

Figure 1

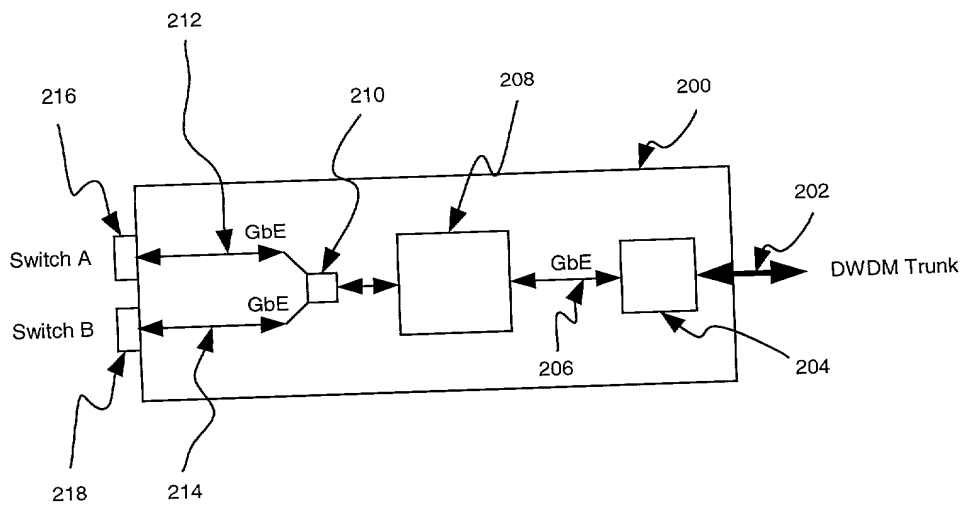


Figure 2

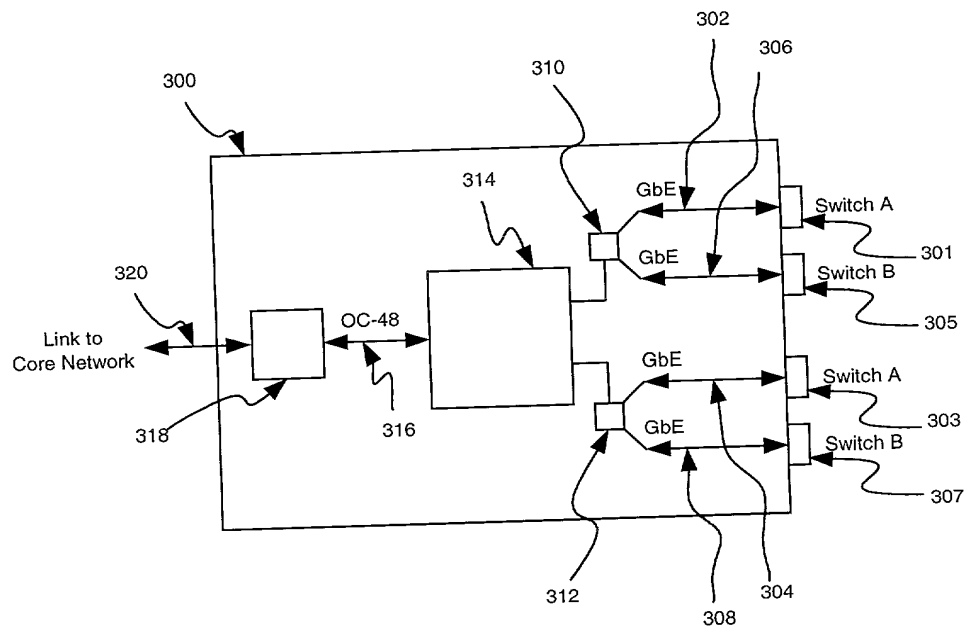


Figure 3

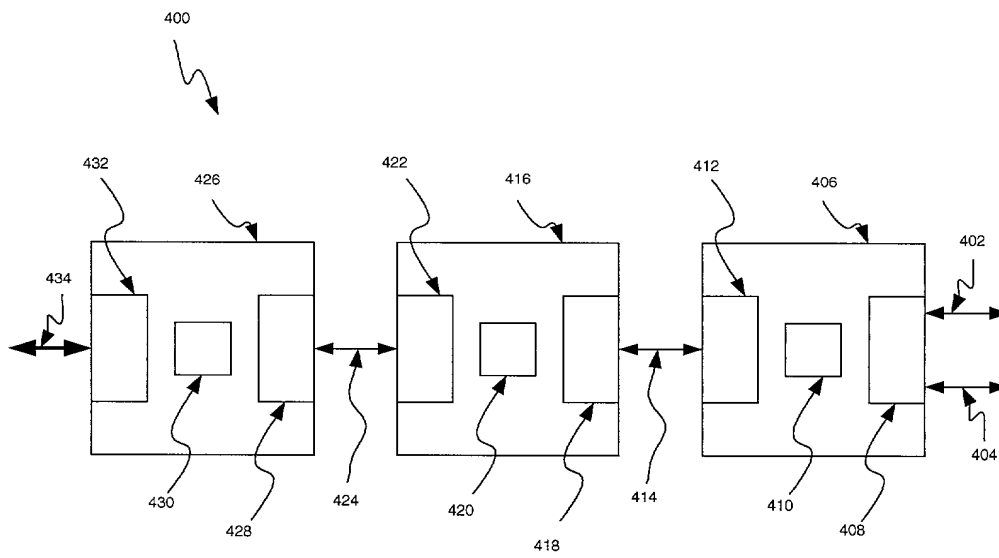


Figure 4

Figure 4 is a schematic diagram of a system 400. The system 400 includes three stages. Each stage includes three parallel processing blocks (430, 428, 426) and a central control block (424). The stages are interconnected sequentially. External inputs and outputs are shown as 434, 402, and 404.

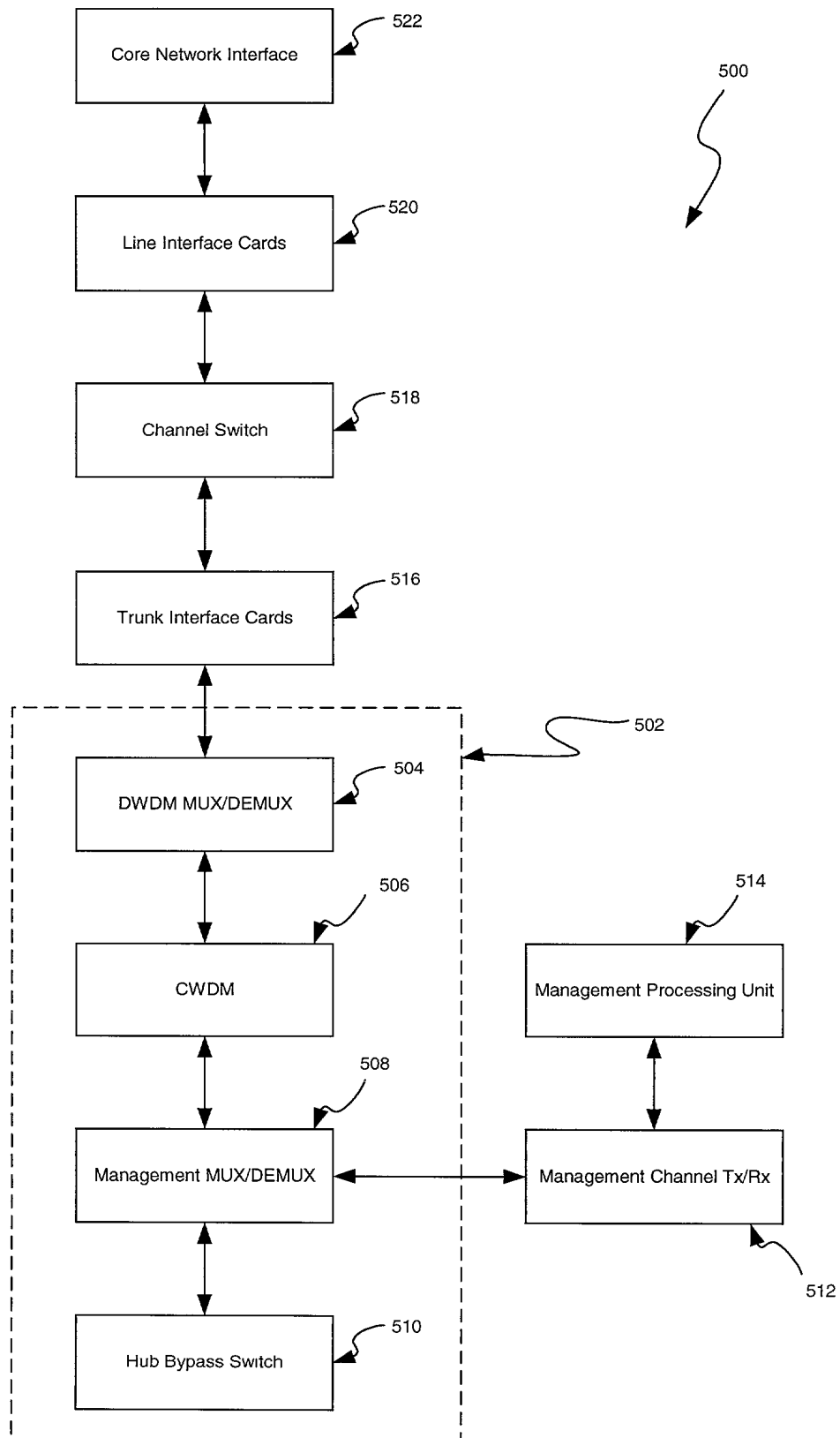


Figure 5

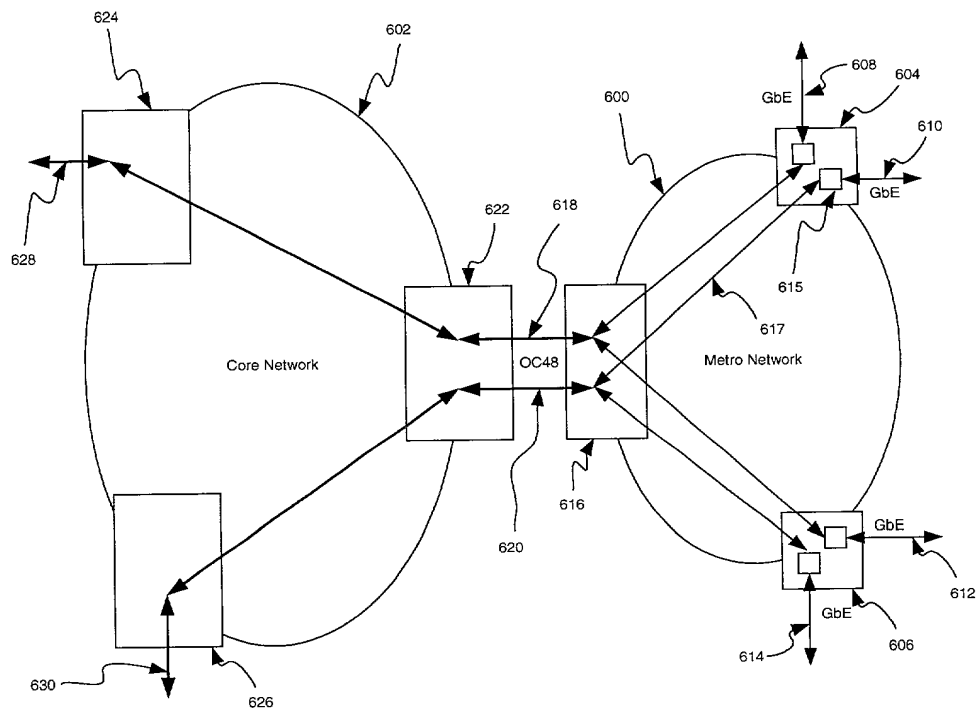


Figure 6